		1	- 12	~
	Application No.	Applicant(s))
Notice of Allewshility	09/997,665	IIJIMA ET AL.		<u>/</u>
Notice of Allowability	Examiner	Art Unit		
	Edna Wong	1753		
The MAILING DATE of this communication appearance All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this ap or other appropriate communication IGHTS. This application is subject to	plication. If not include the mailed in during the mailed the mailed in during the mailed in during the mailed the m	uded ue course.	THIS initiative
1. \boxtimes This communication is responsive to <u>Amendment After Fin</u>	al dated April 23, 2004.			
2. The allowed claim(s) is/are <u>1-14</u> .				
3. \boxtimes The drawings filed on <u>26 November 2001</u> are accepted by	the Examiner.			
 4. Acknowledgment is made of a claim for foreign priority una) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). 	been received. been received in Application No		cation fron	n the
* Certified copies not received:				
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	of this communication to file a reply IENT of this application.	complying with the	requireme	nts
5. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give			NOTICE	OF
6. CORRECTED DRAWINGS (as "replacement sheets") mus	st be submitted.			
(a) ☐ including changes required by the Notice of Draftspers	on's Patent Drawing Review (PTO-	948) attached		
1) hereto or 2) to Paper No./Mail Date				
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date	s Amendment / Comment or in the C	Office action of		
Identifying indicia such as the application number (see 37 CFR 1. each sheet. Replacement sheet(s) should be labeled as such in t	.84(c)) should be written on the drawir he header according to 37 CFR 1.121(ngs in the front (not t d).	the back) o	F
7. DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT			I. Note the	
Attachment(s) 1. ☐ Notice of References Cited (PTO-892)	E III Niedies at the term at the		OTO 450'	
Notice of References Cited (PTO-692) Notice of Draftperson's Patent Drawing Review (PTO-948)	5. ☐ Notice of Informal P6. ☐ Interview Summary		10-152)	
	Paper No./Mail Dat	te		
3. Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date				
4. Examiner's Comment Regarding Requirement for Deposit	8. 🛛 Examiner's Stateme	ent of Reasons for A	llowance	
of Biological Material	9.	Edna Wong Primary Examine Art Unit: 1753	5	

Art Unit: 1753

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The application has been amended as follows:

IN THE Claims

Claim 6, line 16, the word "wall" has been amended to the word -- walls --.

Claim 12, line 4, the word "claims" has been amended to the word -- claim --.

Claim 13, line 1, the word "(Amended)" has been amended to the words -- (Currently amended) --.

Claim 14, line 1, the word "(Amended)" has been amended to the words -- (Currently amended) --.

REASONS FOR ALLOWANCE

The following is an examiner's statement of reasons for allowance:

Claims **1-5** are allowable over the prior art of record because the prior art does not teach or suggest a process of manufacturing a wiring board, said process

Art Unit: 1753

comprising the steps of making, coating, electroplating and polishing as presently claimed, esp., the step of making a resin plate which has wiring pattern recesses with inner walls therein and via through holes using a pair of molds.

Claims **6-8** are allowable over the prior art of record because the prior art does not teach or suggest a process for manufacturing a multi-layer wiring board, said process comprising the steps of (a) manufacturing, (b) forming, (c) coating, (d) electroplating and (e) polishing as presently claimed, esp., the step of making a resin plate having wiring pattern recesses with inner walls therein and having via through holes using a pair of molds.

Claims **9-13** are allowable over the prior art of record because the prior art does not teach or suggest a process for manufacturing a wiring board, said process comprising the steps of (a) preparing, (b) forming, (c) coating, (d) electroplating and (e) polishing as presently claimed, esp., the step of forming said resin layer with wiring pattern recesses having inner walls and with via through holes using a mold.

Claim **14** is allowable over the prior art of record because the prior art does not teach or suggest a process for manufacturing a multi-layer wiring board, said process comprising the steps of (a) preparing, (b) forming, (c) coating, (d) electroplating and (e) polishing as presently claimed, esp., the step of (a) preparing a wiring board which is made in accordance with the steps defined in claim 9.

The prior art does not contain any language that teaches or suggests the above.

Odaira et al. does not teach using a mold to form wiring pattern recesses having inner

Art Unit: 1753

walls and via through holes <u>in the resin</u>. Therefore, a person skilled in the art would not have been motivated to adopt the above conditions, and a prima facie case of obviousness cannot be established.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Edna Wong whose telephone number is (571) 272-1349. The examiner can normally be reached on Mon-Fri 7:30 am to 5:00 pm, alt. Fridays off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nam Nguyen can be reached on (571) 272-1342. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic

Art Unit: 1753

Business Center (EBC) at 866-217-9197 (toll-free).

Edna Wong Primary Examiner Art Unit 1753

EW April 23, 2004